

# Solution-Prepared Hybrid-Nanoparticle Dielectrics for High-Performance Low-Voltage Organic Thin-Film Transistors

Ye Gan,<sup>†</sup> Qin Jia Cai,<sup>†</sup> Chang Ming Li,\* Hong Bin Yang, Zhi Song Lu, Cheng Gong, and Mary B. Chan-Park\*

School of Chemical and Biomedical Engineering, Nanyang Technological University, 62 Nanyang Drive, Singapore 637459, Singapore

**ABSTRACT** Oleic acid capped barium strontium titanate (OA-BST) nanoparticles were synthesized for solution-prepared dielectrics in organic thin-film transistors (OTFTs). The as-synthesized nanoparticles were well-dispersed in organic solvents to deposit very homogeneous dielectric films by direct spin coating. Bottom-gate pentacene TFTs fabricated using these nanoparticle dielectric films showed high mobilities of  $1\text{--}2\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  with on/off ratios of  $10^3$  under a low driven voltage of  $-2.5\text{ V}$ . Top-gate poly(3,3'-didodecylquaterthiophene) (PQT-12) TFTs with nanoparticle dielectrics also exhibited a low-voltage operation ( $-5\text{ V}$ ) performance with mobilities of  $0.01\text{--}0.1\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  and on/off ratios of  $10^3\text{--}10^4$ . Detailed studies on the gate voltage-dependent mobility of the devices showed that only a low gate electric field needed to achieve the saturated mobility for the OA-BST-based pentacene OTFTs could be attributed to the low trapped-state densities ( $<3.9 \times 10^{11}\text{ cm}^{-2}$ ) at the dielectric/semiconductor interfaces for these devices.

**KEYWORDS:** solution-processed • dielectrics • organic thin-film transistors • pentacene • trapped-state density

## INTRODUCTION

Organic thin-film transistors (OTFTs) have been studied extensively for promising applications in large-area electronics at low cost (1). Great efforts have been made to improve the performance of OTFTs, which is generally benchmarked against that of amorphous silicon (a-Si) thin-film transistors (TFTs) with a field-effect mobility  $\mu$  of  $\sim 1\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  (2). In OTFTs, semiconductor/dielectric pairs are essential for device performance because the charge transport is confined to several monolayers of semiconductor molecules at semiconductor/dielectric interfaces (3–6). Various dielectric materials, such as polymers (7–9), inorganic high- $k$  materials (10, 11), and inorganic/polymer blends (12–15), have been incorporated into semiconductor/dielectric pairs for OTFTs; however, it still remains a great challenge for the dielectric materials to meet the requirements of solution processability, flexibility, low-voltage operation ( $\leq 10\text{ V}$ ), and high charge mobility ( $\mu \geq 1\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$ ).

One of the requirements for low-cost dielectric materials is solution processability. Researchers and engineers usually turn to polymeric materials, such as poly(vinylpyrrolidone) (PVP), polystyrene (PS), poly(methyl methacrylate) (PMMA), etc., which possess excellent solution processability and film flexibility but low dielectric constants (9). The low-power-consumption requirement can be met by employing inorganic thin films or solution-processed polymer/inorganic

nanoblending or reducing the film thickness of the polymer to nanoscale. Inorganic thin films with large capacitance are usually deposited with vacuum systems at high cost. An anodization process becomes more attractive to produce pinhole-free metal oxide thin films at room temperature (10, 16, 17). However, the high-energy surfaces of the metal oxides are incompatible with the subsequent deposition of high-quality organic semiconductors (highly crystalline) (10, 11, 18). Polymer thin films in nanoscale thickness can be problematic because of a pinhole problem and need to be highly cross-linked. On the other hand, the homogeneous distribution of inorganic nanoparticles in a polymer matrix is very challenging. To ensure uniform and stable dielectric properties, dispersion of the inorganic nanoparticles is an important factor (19). An alternative approach for ultrathin and high-capacitance dielectrics is atomic layer deposition, which allows for deposition of highly conformal, defect-free dielectric layers on various substrates at relatively low temperature (20, 21). OTFTs with these dielectric materials generally gave field-effect mobility of less than  $1\text{ cm}^2\text{ V}^{-1}\text{ s}^{-1}$  at low-voltage operation (12, 15, 22). The low-voltage-driven and high-performance requirements for OTFTs are possibly associated with the trapped-state density at semiconductor/dielectric interfaces. At trapped sites such as Si–OH species at the semiconductor/dielectric interfaces, charge carriers may be trapped during transportation in the interface, leading to high-voltage-driven and poor device performance (23, 24). It has been reported that improvement of the field-effect mobility of the pentacene TFTs could be experimentally attributable to the reduction of the reactive sites at the interface, such as the alkylsilane treatment of the SiO<sub>2</sub> surface before pentacene deposition (25, 26).

\* Corresponding author. E-mail: ecml@ntu.edu.sg (C.M.L.), mbechan@ntu.edu.sg (M.B.C.-P.).  
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<sup>†</sup> These authors contributed equally to this work.

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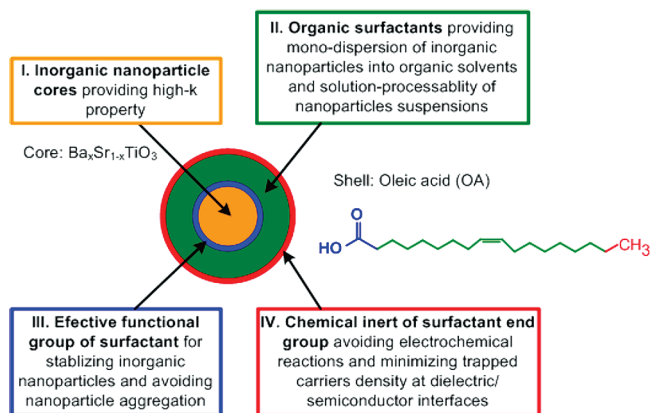


FIGURE 1. Schematic illustration of core–shell hybrid nanoparticles for dielectric application in OTFTs.

To fulfill the above requirements of dielectric materials for OTFTs, a novel dielectric material of an oleic acid capped barium strontium titanate (OA-BST) core–shell hybrid nanoparticle was designed and synthesized according to the following principles (as illustrated in Figure 1): (I) high- $k$  inorganic nanoparticle cores can provide a high- $k$  property, which facilitates charge accumulation in the channel; (II) organic surfactants, such as oleic acid, capping the surfaces of inorganic nanoparticle cores can enable well-dispersion of the inorganic nanoparticles in organic solvents and solution processability of nanoparticle suspensions; (III) effective functional groups of organic surfactants can stabilize the inorganic nanoparticles and avoid nanoparticles aggregation; (IV) the chemical inertness of the surfactant end group (pointing out from inorganic nanoparticle surfaces) may avoid possible electrochemical interactions and minimize the trapped-state density at the semiconductor/dielectric interfaces.

The OA-BST nanoparticles with a molar formula  $\text{Ba}_x\text{Sr}_{1-x}\text{TiO}_3$  in which  $x$  varies as 0.1, 0.2, and so on, until 0.9 mol %, were synthesized and characterized by high-resolution transmission electron microscopy (HRTEM) measurements. The nanoparticles were further well-dispersed in an organic solvent to fabricate uniform thin films by spin coating. The dielectric constants of the films with different Ba/Sr molar ratios of the nanoparticles were characterized. Bottom-gate pentacene and top-gate poly(3,3'-didodecylquaterthiophene) (PQT-12) TFTs were fabricated using these nanoparticle dielectric films to investigate the electric transport performance. The gate voltage-dependent mobilities of these devices were studied to explore the possible mechanism of the electric transport at the dielectric/semiconductor interface.

## EXPERIMENTAL SECTION

**Materials.** Barium oxide (97%), strontium oxide (99.9%), titanium isopropoxide (97%), diphenyl ether (99%), oleic acid (90%), ethylene glycol (99.8%, anhydrous), 2-propanol (99.9%), a 30% hydrogen peroxide solution in water, toluene (HPLC), pentacene (99.99%), and ethanol ( $\geq 99.8\%$ ) were obtained from Sigma-Aldrich and used as received without further purification. The poly(3,3'-didodecylquaterthiophene) (PQT-12)/dichlorobenzene solution (0.3 wt %) was provided by Xerox Research Centre of Canada.

**Synthesis of Oleic Acid Capped Barium Strontium Titanate (OA-BST).** Barium titanium glycolate and strontium titanium glycolate complexes were prepared separately according to the literature (27). The two precursors of overall 5 mmol with systematically adjusted  $\text{Ba}^{2+}/\text{Sr}^{2+}$  molar ratios were transferred into a dry mixture of 50 mL of diphenyl ether and 5 mmol of stabilizing agent OA at 100 °C under an argon atmosphere. A total of 1.8 g of a 30 wt % hydrogen peroxide solution was injected slowly into the solution (vigorous exothermic reaction) to avoid a possible splashing hazard caused by boiling. The solution was maintained in a closed system and stirred at 100 °C over 48 h to promote further hydrolysis and the formation of the OA-capped nanoparticles. After the reaction, the solution was cooled to room temperature and the nanoparticles were readily precipitated with excess ethanol. The precipitate was washed six times with ethanol to remove absorbed OA with the assistance of a centrifuge.

**Material Characterization.** The surface properties of the dried nanoparticles were characterized by a Thermo Nicolet 5700 FTIR spectrometer, and the attenuated total reflectance Fourier transform infrared (ATR-FTIR) spectra were collected with a resolution of  $1\text{ cm}^{-1}$ . The surface morphology and  $R_{\text{rms}}$  values were measured by an atomic force microscope (Digital Instruments Nanoscope IIIa MultiMode scanning probe microscope) in tapping mode with a scan rate of 0.5 Hz. The pentacene films were studied by X-ray diffraction (XRD) in the symmetric-reflection-coupled  $\theta-2\theta$  arrangement. XRD patterns were recorded at room temperature on a Bruker AXS X-ray diffractometer using  $\text{Cu K}\alpha$  radiation ( $\lambda = 1.5418\text{ \AA}$ ).

**Device Fabrication and Characterization. Capacitors.** Metal–insulator–metal (MIM) capacitors of indium–tin oxide (ITO)/OA-BST nanoparticle film/Al were fabricated. OA-BST nanoparticle films were formed on ITO-coated glasses by spin coating of the nanoparticle suspensions in toluene ( $\sim 40\text{ mg/g}$ ). Silver top contacts of  $\sim 100\text{ nm}$  thickness and  $4\text{ mm}^2$  area were deposited on the nanoparticle films under  $10^{-7}$  Torr. The capacitors were characterized using an HP 4284 Precision LCR meter.

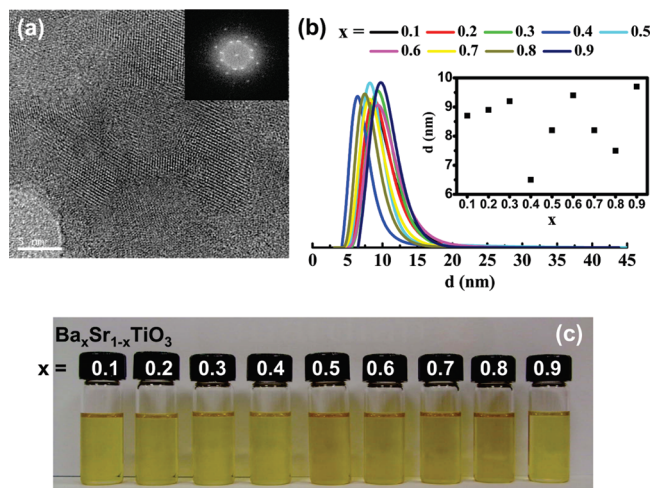
**Pentacene Transistors.** Bottom-gate pentacene TFTs were fabricated by the thermal evaporation of pentacene films of 40 nm onto the OA-BST nanoparticle films on ITO/PET or Al/SiO<sub>2</sub> substrates under  $10^{-7}$  Torr. Gold source-drain electrodes of 100 nm were subsequently defined on the pentacene films through a shadow mask. In addition, bottom-gate pentacene TFTs were also fabricated on a heavily doped n-type (n++) silicon wafer with a 100 nm top layer of SiO<sub>2</sub> as the insulator. The SiO<sub>2</sub> surfaces were treated with 10 mmol of an octyltrichlorosilane (OTS)/hexane solution for 30 min before pentacene deposition.

**PQT-12 Transistors.** PQT-12 TFTs were fabricated on an n++ silicon wafer with 100 nm thermally grown SiO<sub>2</sub> as the bottom-gate insulator. Gold/titanium (Au/Ti: 50 nm/1 nm) source-drain contacts were patterned using a lift-off technique. PQT-12 semiconductor layers of about 60 nm were deposited on OTS-treated substrates, subsequently annealed at 125 °C for 30 min, and cooled to room temperature in vacuum. The bottom-gate geometry was used to characterize the performance of PQT-12/OTS-SiO<sub>2</sub> TFTs. The top-gate devices were made by spin coating of OA-BST nanoparticle suspensions in toluene ( $\sim 40\text{ mg/g}$ ) on PQT-12 semiconductor layers, followed by deposition of  $\sim 100\text{ nm}$  silver on the nanoparticle dielectric films under  $10^{-7}$  Torr as the top-gate electrode.

**Transistor Characterizations.** The electrical properties of the fabricated OTFTs were measured with a Cascade Microtech Summit 11751B-6 probe station and an Agilent 4157B semiconductor parameter analyzer.

## RESULTS AND DISCUSSION

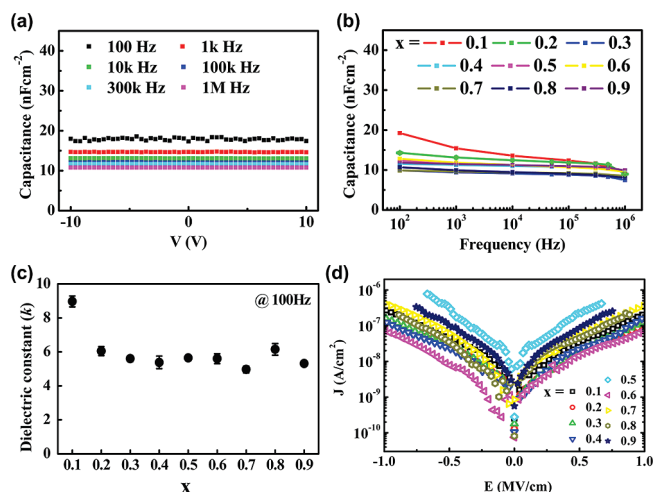
Figure 2a shows the HRTEM image of OA-Ba<sub>0.1</sub>Sr<sub>0.9</sub>TiO<sub>3</sub> nanoparticles, and the inset is the corresponding selected-



**FIGURE 2.** (a) HRTEM image of OA-Ba<sub>0.1</sub>Sr<sub>0.9</sub>TiO<sub>3</sub> nanoparticles. The inset is the corresponding SAED pattern. (b) OA-Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> nanoparticle size distribution from  $\zeta$ -potential measurements. The inset is the mean size for different nanoparticles. (c) Digital images of clear OA-Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> nanoparticle suspensions in toluene ( $\sim$ 40 mg/g), where  $x = 0.1, 0.2,$  and so on, until 0.9 from left to right.

area electron diffraction (SAED) pattern. Lattice structures can be observed and identified from the cores of the nanoparticles. The size of the nanoparticles is 5–8 nm. From  $\zeta$ -potential measurements, the size of OA-Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> ( $x = 0.1, 0.2, \dots, 0.9$ ) nanoparticles is in the range of 6–10 nm, which is very close to the theoretical value in terms of the nanoparticle structure shown in Figure 2b. Figure 2c shows that very clear nanoparticle dispersions in nonpolar organic solvents (such as toluene) are produced. The dispersions are stable for more than 1 year without gelatinization or agglomerates. However, if the nanoparticles were dried, it would be hard for them to redisperse into the organic solvent, as discussed in our previous work (28). Thin films (300–400 nm thickness) obtained from spin coating of the nanoparticle suspensions are homogeneous and smooth, with a surface root-mean-square roughness ( $R_{\text{rms}}$ ) ranging from 3.5 to 5.5 nm. The surfaces are hydrophobic with water contact angles of 93–105°.

It is known that the capacitance of a capacitor is more prominent at lower frequencies. The typical frequencies for characterizing the gate dielectric materials in organic transistors are in a range over  $10^2$ – $10^6$  Hz (29, 30). The capacitance of the MIM capacitors was measured with a direct-current sweep voltage of  $\pm 10$  V at fixed frequencies in the range of  $10^2$ – $10^6$  Hz (Figure 3b), indicating that the capacitance only decreases slightly with an increase of the frequency. Figure 3a gives the relationship of capacitance versus voltage for the OA-Ba<sub>0.1</sub>Sr<sub>0.9</sub>TiO<sub>3</sub> film. No polarization behavior is observed for all OA-BST films. This is different from the highly crystallized perovskite oxides that normally possess the ferroelectric property (31). The dielectric constant  $k$  values at 100 Hz calculated by  $k = C_i t / \epsilon_0$  (where  $\epsilon_0$ ,  $C_i$ , and  $t$  are the vacuum permittivity, unit area capacitance, and film thickness, respectively) for different OA-BST nanoparticles are shown in Figure 3c, indicating that OA-Ba<sub>0.1</sub>Sr<sub>0.9</sub>TiO<sub>3</sub> has a dielectric constant of  $\sim$ 9.6. With increasing Ba<sup>2+</sup> loading with a reduction of the Sr<sup>2+</sup> contents in the

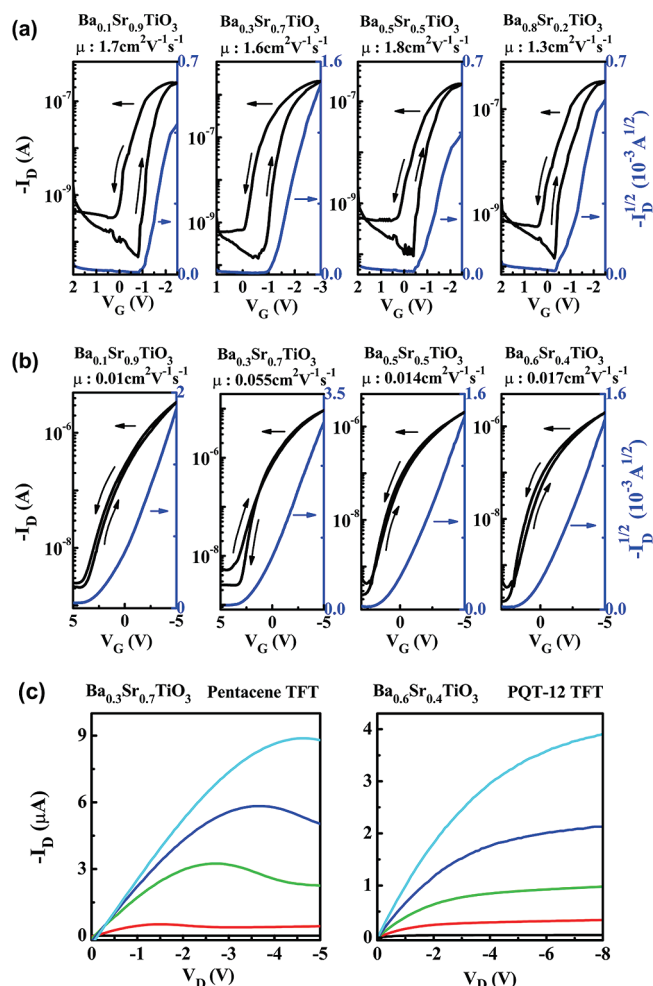


**FIGURE 3.** Electrical characteristics of spin-coated OA-BST nanoparticle films: (a) plots of capacitance versus voltage for the OA-Ba<sub>0.1</sub>Sr<sub>0.9</sub>TiO<sub>3</sub> film ( $\sim$ 410 nm) at different frequencies; (b) plots of capacitance versus frequency in the range of  $10^2$ – $10^6$  Hz for OA-BST films (380–510 nm); (c) comparison of  $k$  measured at  $10^2$  Hz; (d) current densities versus electric field plots.

compound,  $k$  drops to 6.1 for OA-Ba<sub>0.2</sub>Sr<sub>0.8</sub>TiO<sub>3</sub> and then 5.6 for OA-Ba<sub>0.3</sub>Sr<sub>0.7</sub>TiO<sub>3</sub>. Films with OA-Ba<sub>0.4</sub>Sr<sub>0.6</sub>TiO<sub>3</sub> to OA-Ba<sub>0.9</sub>Sr<sub>0.1</sub>TiO<sub>3</sub> exhibit dielectric constants in a range over 5–6. This result suggests that small differences in the Ba/Sr molar ratio of the nanoparticles at low values ranging from 0.1 to 0.4 can result in significant  $k$  changes of the nanoparticle films. The spin-coated OA-BST nanoparticle films (350–450 nm) show leakage current densities in the range of  $10^{-8}$ – $10^{-6}$  A cm<sup>-2</sup> when the electric field across the capacitor is 0.7–1 MV cm<sup>-1</sup> (Figure 3d).

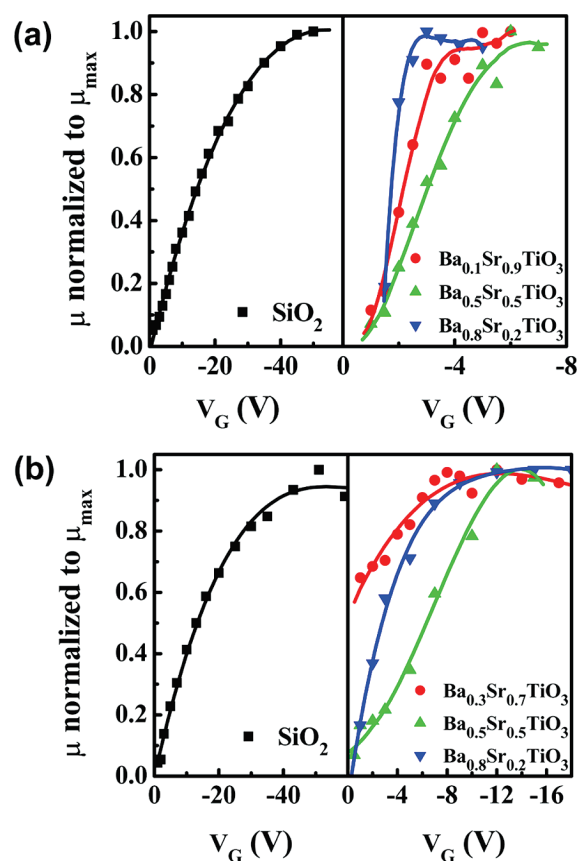
Parts a and b of Figure 4 give a set of representative transfer curves for both forward and reverse gate bias scans (scan rate:  $\sim$ 0.7 V s<sup>-1</sup>) and the  $|I_{\text{D}}|^{1/2}$  vs  $V_{\text{G}}$  plots for the forward condition of bottom-gate pentacene TFTs and top-gate PQT-12 TFTs, respectively. Both bottom- and top-gate TFTs have selected OA-BST nanoparticles as gate dielectrics. The OA-BST-based pentacene TFTs exhibit the field-effect mobilities of 1–2 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> with on/off ratios of  $10^3$  at the low operating voltage  $V_{\text{D}} = V_{\text{G}} = -2.5$  V. PQT-12 TFTs operated under higher voltage  $V_{\text{D}} = V_{\text{G}} = -5$  V have field-effect mobilities of 0.01–0.1 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and on/off ratios of  $10^3$ . The corresponding output characteristics of the devices in Figure 4c clearly demonstrate the typical linear/saturation behavior. The hysteresis of  $I_{\text{D}}-V_{\text{G}}$  is observed (in Figure 4a) with a positive shift in  $V_{\text{th}}$  ( $\Delta V_{\text{th}} = 0.6$ – $0.9$  V), probably due to the trapping of electrons injected from the gate (gate leakage) (32). Hysteresis is often observed in OTFTs with high- $k$  polymeric dielectric materials, such as poly(vinyl alcohol) (33), polymer electrolyte (34), as well as sol-gel silica dielectrics (35), which is probably due to the trapping of electrons injected from the gate. The hysteresis can be suppressed effectively by lowering the scan rate (36).

The device mobility is known to be gate-voltage-dependent in OTFTs (3, 37). The dependency of the mobility of both pentacene and PQT-12 TFTs with OA-BST dielectrics on different gate voltages was studied to explore the fundamental insight of the OTFT mobility. For comparison, the



**FIGURE 4.** Representative transfer characteristics and  $|I_D|^{1/2}$  vs  $V_G$  plots of OTFTs with OA-BST nanoparticle films as gate dielectrics. (a) Bottom-gate pentacene TFTs, channel width/length ( $W/L$ ) = 11, 45, 13, and 20 for samples  $\text{Ba}_{0.1}\text{Sr}_{0.9}\text{TiO}_3$  ( $21 \text{ nF cm}^{-2}$ ),  $\text{Ba}_{0.3}\text{Sr}_{0.7}\text{TiO}_3$  ( $9.8 \text{ nF cm}^{-2}$ ),  $\text{Ba}_{0.5}\text{Sr}_{0.5}\text{TiO}_3$  ( $16.1 \text{ nF cm}^{-2}$ ), and  $\text{Ba}_{0.8}\text{Sr}_{0.2}\text{TiO}_3$  ( $16.8 \text{ nF cm}^{-2}$ ), respectively. (b) Top-gate PQT-12 TFTs, channel width/length ( $W/L$ ) = 900 for  $\text{Ba}_{0.1}\text{Sr}_{0.9}\text{TiO}_3$  ( $21.8 \text{ nF cm}^{-2}$ ),  $\text{Ba}_{0.3}\text{Sr}_{0.7}\text{TiO}_3$  ( $10.1 \text{ nF cm}^{-2}$ ),  $\text{Ba}_{0.5}\text{Sr}_{0.5}\text{TiO}_3$  ( $12.8 \text{ nF cm}^{-2}$ ), and  $\text{Ba}_{0.6}\text{Sr}_{0.4}\text{TiO}_3$  ( $12.7 \text{ nF cm}^{-2}$ ), respectively. (c) Output characteristics of the selected devices in parts a and b: for the bottom-gate pentacene TFT, the gate voltage increased from 0 to  $-4 \text{ V}$  in steps of  $-1 \text{ V}$ , and for the top-gate PQT-12 TFT, the gate voltage increased from 0 to  $-6.4 \text{ V}$  in steps of  $-1.6 \text{ V}$ , respectively.

investigations of this dependency were also conducted on devices with OTS-SiO<sub>2</sub> dielectrics. To minimize the gate stress effect, 5 min of rest was allowed between two consecutive measurements for each device. Normalized mobility was used to have a better image in which to display how the mobility varied with  $V_G$  for different devices by neglecting the mobility difference caused by the device variation. It was defined as the actual mobility at the corresponding  $V_G$  over the saturated (maximum) mobility obtained from the same device. The mobilities versus gate-voltage dependencies are plotted in Figure 5a,b for pentacene and PQT-12 TFTs, respectively. The saturated mobilities, together with other characteristics of the devices, are listed in Table 1. The pentacene/OA-BST TFTs all demonstrate a mobility plateau ( $1\text{--}3.5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ ) at much lower driven voltages around  $-5 \text{ V}$  compared to the pentacene/OTS-SiO<sub>2</sub>



**FIGURE 5.** Gate-dependence mobility of the OTFTs with OA-BST nanoparticle films as gate dielectrics. (a) Bottom-gate pentacene TFTs, channel width/length ( $W/L$ ) = 24, 11, 13, and 20 for samples OTS-SiO<sub>2</sub>, OA- $\text{Ba}_{0.1}\text{Sr}_{0.9}\text{TiO}_3$ , OA- $\text{Ba}_{0.5}\text{Sr}_{0.5}\text{TiO}_3$ , and OA- $\text{Ba}_{0.8}\text{Sr}_{0.2}\text{TiO}_3$ , respectively. (b) Top-gate PQT-12 TFTs, channel width/length ( $W/L$ ) = 90, 180, 90, and 90 for samples OTS-SiO<sub>2</sub>, OA- $\text{Ba}_{0.3}\text{Sr}_{0.7}\text{TiO}_3$ , OA- $\text{Ba}_{0.5}\text{Sr}_{0.5}\text{TiO}_3$ , and OA- $\text{Ba}_{0.8}\text{Sr}_{0.2}\text{TiO}_3$ , respectively.

TFTs, where the mobility plateau is not observed even at the gate voltage of  $-50 \text{ V}$  and the highest mobility is only  $\sim 0.5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ . It can be seen that the OA-BST-based pentacene TFTs exhibit superior performance over the pentacene TFTs on OTS-SiO<sub>2</sub>. The morphology and crystallinity of the pentacene films on the respective substrates were examined using atomic force microscopy and XRD. The pentacene films illustrate a similar structure on OA-BST nanoparticle films and OTS-SiO<sub>2</sub> surfaces (Figure S1 in the Supporting Information). The mobility plateau occurs at a driven voltage of around  $-12 \text{ V}$  for top-gate PQT-12/OA-BST TFTs ( $0.03\text{--}0.04 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ ), while taking place at a high operating voltage ( $-55 \text{ V}$ ) for the bottom-gate PQT-12/OTS-SiO<sub>2</sub> TFTs ( $\sim 0.09 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ ). The lower device  $\mu_{\text{max}}$  of the top-gate PQT-12 TFTs may be related to the high surface roughness of the PQT-12 films (38). Rougher semiconductor surfaces and probable moistures adsorbed at the interface during fabrication in an atmospheric environment could degrade the device mobility (39–41).

The dependency of field-effect mobility on the gate voltage could be described by a multiple trapping and release model (37, 42), which has been generally used to describe the charge-transport behavior in OTFTs. According to this model, there are two important factors significantly influencing the channel mobility: one is the number of charge

**Table 1. Performance of Bottom-Gate Pentacene and Top-Gate PQT-12 TFTs**

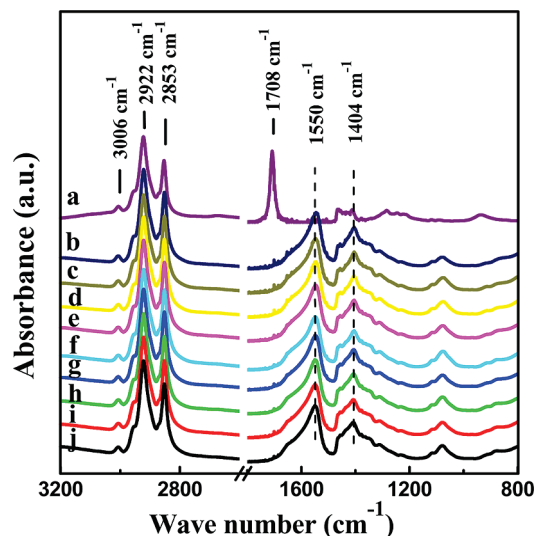
organic semiconductor	dielectric	dielectric thickness (nm)	$\mu_{\max}$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> )	$V_G$ (V)	$C_i$ (nF cm <sup>-2</sup> )	$V_{th}$ (V)	$S$ (V dec <sup>-1</sup> )	$N_{trap}^{\max}$ (cm <sup>-2</sup> )
pentacene	OTS-SiO <sub>2</sub>	100	0.54	-50	34	-9.8	0.778	$2.06 \times 10^{12}$
	Ba <sub>0.1</sub> Sr <sub>0.9</sub> TiO <sub>3</sub>	420	2.7	-5	21	-2.5	0.232	$3.88 \times 10^{11}$
	Ba <sub>0.5</sub> Sr <sub>0.5</sub> TiO <sub>3</sub>	350	3.42	-5	16.1	-1.14	0.216	$2.7 \times 10^{11}$
	Ba <sub>0.8</sub> Sr <sub>0.2</sub> TiO <sub>3</sub>	350	1.38	-3	16.8	-0.8	0.277	$3.9 \times 10^{11}$
PQT	OTS-SiO <sub>2</sub>	100	0.089	-55	34	-9	0.989	$3.37 \times 10^{12}$
	Ba <sub>0.3</sub> Sr <sub>0.7</sub> TiO <sub>3</sub>	505	0.04	-12	10.1	2.8	2.05	$2.15 \times 10^{12}$
	Ba <sub>0.5</sub> Sr <sub>0.5</sub> TiO <sub>3</sub>	420	0.03	-13	12.8	-0.73	1.26	$1.64 \times 10^{12}$
	Ba <sub>0.8</sub> Sr <sub>0.2</sub> TiO <sub>3</sub>	495	0.04	-12	11.3	0.4	1.08	$1.23 \times 10^{12}$

carriers induced, which can be denoted as the unit area carrier charge ( $Q_s$ ), and another is the number of trapped states to be filled. Usually, one is more concerned with the first factor because higher  $k$  insulators could achieve a sufficient number of charge carriers to fill all of the trapped states at lower gate voltages (42). Interestingly, in our experimental results, as shown in Table 1, the OA-BST dielectrics have a unit area capacitance of 10–21 nF cm<sup>-2</sup>, which is much lower than those of the high- $k$  materials (80 nm Al<sub>2</sub>O<sub>3</sub>, 90 nF cm<sup>-2</sup> (3); 270 nm TiO<sub>2</sub>/PVP/PαMS, 34.4 nF cm<sup>-2</sup> (12); 250 nm ZrO<sub>2</sub>-OTMS, ~68 nF cm<sup>-2</sup> (22); HfO<sub>2</sub>, 0.69 μF cm<sup>-2</sup> (43)) and even SiO<sub>2</sub> (100 nm: ~34 nF cm<sup>-2</sup>). The gate-voltage-accumulated charge carriers known to be confined very close to the semiconductor/insulator interfaces should be much smaller in the pentacene/OA-BST system than those dielectric systems reported for low-voltage high-performance devices (3, 12, 22, 43). However, the mobility of the pentacene/OA-BST TFTs is almost 1 order higher and under less than 1 order lower of the gate field ( $E$ ). Very likely, the trapped-state densities at the semiconductor/insulator interfaces play the key role in the high-mobility and low-voltage performance of OA-BST-based pentacene devices.

The maximum interface trapped-state density can be estimated using  $N_{trap}^{\max} = (C_i/q)[(qS \log e/k_B T) - 1]$  (44, 45), where  $k_B$ ,  $T$ ,  $e$ ,  $q$ ,  $S$ , and  $C_i$  are Boltzmann's constant, the temperature, the base of the natural logarithm, the electron charge, the reciprocal subthreshold swing, and the dielectric capacitance per unit area, respectively. The calculated maximum trapped-state densities of the devices at the maximum mobility condition are also summarized in Table 1. Clearly,  $S$  and the threshold voltages ( $V_{th}$ ) for the pentacene/OA-BST devices are much better than those for the pentacene/OTS-SiO<sub>2</sub> devices. The pentacene/OA-BST devices have maximum trapped-state densities in the range of  $(2.7\text{--}3.9) \times 10^{11}$  cm<sup>-2</sup>, almost 1 order lower than those for OTS-SiO<sub>2</sub> devices ( $2.06 \times 10^{12}$  cm<sup>-2</sup>). The top-gate PQT/OA-BST devices exhibit higher maximum trapped-state densities in the range of  $(1\text{--}2) \times 10^{12}$  cm<sup>-2</sup>, which could be the origin for the lower  $\mu_{\max}$  and higher subthreshold swings obtained on these devices as discussed above. However, these values are still less than those of bottom-gate PQT/OTS-SiO<sub>2</sub> devices ( $3 \times 10^{12}$  cm<sup>-2</sup>). Fundamentally, fewer numbers of charge carriers are needed to fill the localized states in the devices with low trapped-state densities, and low-voltage operation with high mobility could be achieved on the OA-BST dielectric-based OTFTs. In comparison to cross-linked PVP dielectric films with similar capacitance per unit area (10–20 nF cm<sup>-2</sup>)

(46) used in OTFTs, the OA-BST dielectrics have advantages of enabling much lower voltage for device operation. In addition, the dielectrics developed in this work do not need the postannealing for cross-linking required by the polymer-based ones. These results suggest that the trapped-state density at semiconductor/dielectric interfaces is important for enabling low-voltage high-performance OTFTs and should also be taken into consideration during the design of the proper dielectric materials for OTFTs.

In Figure 6a–j, the FTIR spectra of OA and OA-Ba<sub>*x*</sub>-Sr<sub>1-*x*</sub>TiO<sub>3</sub> nanoparticles for all samples display two strong peaks at 2922 and 2853 cm<sup>-1</sup>, which are assigned to the intense antisymmetric and symmetric C–H stretching vibrations of the –CH<sub>2</sub>– groups in the hydrocarbon moiety (47). The weak absorption band at 3006 cm<sup>-1</sup>, attributable to the olefinic C–H stretching vibration (48), is also clearly observable. The strong adsorption peak at 1708 cm<sup>-1</sup> shown in Figure 6a, associated with free C=O stretching vibration, is absent in the spectra for OA-capped nanoparticles (Figure 6b–j), while two strong characteristic bands centered at 1550 and 1404 cm<sup>-1</sup> appear in Figure 6b–j. These two absorption bands can be characteristically attributed to the COO– antisymmetric and symmetric stretching vibrations of carboxylate anions complexed with the metal surface (48, 49). The results suggest that OA as the stabilizing ligand is effectively bound to surfaces of the inorganic nanoparticle cores and nearly no free OA is present in the nanoparticle



**FIGURE 6.** FTIR spectra of representative (a) OA and (b–j) OA-Ba<sub>*x*</sub>Sr<sub>1-*x*</sub>TiO<sub>3</sub> nanoparticles where  $x = 0.1, 0.2$ , and so on, until 0.9.

suspensions. It has been shown that alkyl chains on modified dielectric surfaces can substantially reduce the interfacial trapped-state density (23, 50, 51), which is principally attributed to interfacial chemical functionalities/species such as Si–OH for SiO<sub>2</sub> dielectrics, in conjunction with adsorbed H<sub>2</sub>O and adventitious carbon contamination (52, 53). It has been found that silanol groups cannot be completely removed by such self-assembled alkyl layers (54). On the other hand, the alkoxide precursors are readily reactive with carboxylic acids to produce OA-BST nanoparticles, thus resulting in significantly lower density of metal–OH species on the nanoparticle surfaces (55, 56). In addition, the near absence of –COOH groups on the nanoparticle surfaces could avoid possible electrochemical reactions and minimize the trapped-state density at the semiconductor/dielectric interface when the charge carriers are transported (26).

## CONCLUSIONS

In summary, solution-processable OA-Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub> nanoparticles, with *x* varying as 0.1, 0.2, and so on, until 0.9 mol %, were synthesized for dielectric applications in low-voltage OTFTs. The dielectric constant of the spin-on OA-BST films varied in the range from 5 to 9.6 with different Ba/Sr molar ratios in the nanoparticles. Bottom-gate pentacene and top-gate PQT-12 TFTs with the OA-BST films as gate insulators were fabricated. The studies on the gate dependence of mobility in the devices show that very high device mobilities (1.4–3.4 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>) could be obtained at low operating voltages (<–5 V) for pentacene TFTs. Low-operating voltages (~–12 V) are also demonstrated on PQT-12 TFTs with a mobility of ~0.1 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and on/off ratios of 10<sup>3</sup>–10<sup>4</sup>. This prominent device performance on the OA-BST nanoparticle dielectrics was investigated to be attributable to the very low trapped-state densities (<3.9 × 10<sup>11</sup> cm<sup>-2</sup>) at the semiconductor/insulator interface, which was more than 1 order lower than that obtained in the OTS- SiO<sub>2</sub> devices.

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**Supporting Information Available:** Surface morphologies and XRD patterns of pentacene films. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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